

SILICON EPITAXIAL

NPN TRANSISTOR

General purpose power transistor for switching and linear applications in a

hermetic TO-39 package.



MECHANICAL DATA

Dimensions in mm (inches)

8.89 (0.35) 9.40 (0.37) 7.75 (0.305) 8.51 (0.335) 6.10 (0.240) 6.60 (0.260) 12.70 (0.500) dia.

0.66 (0.026)

TO-39 PACKAGE

PIN 1 - Emitter

PIN 2 - Base

PIN 3 - Collector

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise stated)

V_{CBO}	Collector – Base Voltage	900V		
V_{CER}	Collector – Emitter Voltage $R = 10\Omega$	900V		
V_{EBO}	Emitter – Base Reverse Voltage	5V		
$I_{\mathbb{C}}$	Continuous Collector Current	2W		
P_{TOT}	Total Device Dissipation $T_C = 25^{\circ}C$	3.5A		
T_J	Maximum Operating Junction Temperature	200°C		
T_{STG}	and Storage Temperature Range	-55 to 200°C		

FEATURES

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise stated)

	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
I _{CBO}	Collector Base Leakage Current	V _{CB} = 900V				0.012	mA
hFE	D.C Current Gain	V _{CE} = 10V	I _C =0.02A	20		180	_
fae					20		MHz

Semelab PIc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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